

# Abstracts

## Design Procedure for High-Efficiency Linear Microwave Power Amplifiers

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*F.N. Sechi. "Design Procedure for High-Efficiency Linear Microwave Power Amplifiers." 1980 Transactions on Microwave Theory and Techniques 28.11 (Nov. 1980, Part I [T-MTT]): 1157-1163.*

An optimal design for a high-efficiency linear amplifier is achieved by a graphical technique, with the active device characterized by load impedance contours for constant power and constant intermodulation distortion (IMD). The use of this method is demonstrated by an example. Also described are the excellent results obtained in an amplifier operating over the frequency range from 3.7 to 4.2 GHz.

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